2001 FINNISH SIGNAL PROCESSING SYMPOSIUM

June 5, 2001

Helsinki University of Technology, Department of Electrical and Communications Engineering
Signal Processing Laboratory, Otakaari 5 A, Espoo, Finland

URL: http://wooster.hut.fi/finsig01/

PROGRAM

HALL S4

8.00–	**Registration**

9.00–9.10 Welcome to FINSIG'01

9.10–10.30 DIGITAL FILTERS AND SYSTEMS

Decimation by Non-Integer Factor Using CIC Filter and Linear Interpolation
Djordje Babic, *Tampere University of Technology, Finland;* Jussi Vesma, *Nokia Research Center, Finland;* Markku Renfors, *Tampere University of Technology, Finland*

A Complex Adaptive Notch Filter Based on the Steiglitz-McBride Method
Yaohui Liu, Timo I. Laakso, *Helsinki University of Technology, Finland;* Paulo S. R. Diniz, *Universidade Federal do Rio de Janeiro, Brazil*

Flat Systems in Discrete Signal Processing
Markku T. Nihtilä, *University of Kuopio, Finland*

Polynomial-Predictive FIR Design – A Review
Jarno M. A. Tanskanen, *Helsinki University of Technology, Finland*

10.30–11.00   Coffee Break

11.00–11.40 COMMUNICATIONS SIGNAL PROCESSING

Adaptive Channel Equalizer for WCDMA Downlink
Kari Hooli, Matti Latva-aho, Markku Juntti, *University of Oulu, Finland*

Space-Frequency Turbo Coded OFDM for Future High Data Rate Wideband Radio Systems
Djordje Tujkovic, Markku Juntti, Matti Latva-aho, *University of Oulu, Finland*

11.40–12.20 MEDICAL SIGNAL PROCESSING

Action Potential Analysis by Real Time DSP Hardware and Software for Odour Exposure Responses
Matti Huotari, Vilho Lantto, *University of Oulu, Finland*

Time-Varying ARMA Modelling of Nonstationary EEG Using Kalman Smoother Algorithm
Mika P. Tarvainen, Perttu O. Ranta-aho, *University of Kuopio; Kuopio University Hospital, Finland;* Pasi A. Karjalainen, *University of Kuopio, Finland*

12.20–13.20   Lunch

13.20–14.40 DSP ARCHITECTURES AND MULTIMEDIA SYSTEMS

Pipeline Architecture for 8×8 IDCT with Fixed-Point Error Analysis
Jari A. Nikara, Rami J. Rosendahl, Jarmo H. Takala, *Tampere University of Technology, Finland*

Efficient Implementation of Multimedia Algorithms on Standard Processors
Prakash Sastry, Irek Defée, *Tampere University of Technology, Finland*

The FutureTV Project: MHP Compliant Software Development
Artur Lugmayr, Seppo Kalli, Teemu Lukkarinen, Arttu Heinonen, Perttu Rautavirta, Mikko Oksanen, Florina Tico, Jens Spieker, Mathew Anurag, *Tampere University of Technology, Finland*

Flexible DSP Platform for Various Workload Patterns
Antti Pelkonen, Jussi Roivainen, Juha-Pekka Soininen, *VTT Electronics, Finland*

14.40–15.10   Coffee Break

15.10–16.30 AUDIO AND IMAGE PROCESSING

Audio Restoration Using Sound Source Modeling
Paulo Esquef, Vesa Välimäki, Matti Karjalainen, *Helsinki University of Technology, Finland*

Equalization and Modeling of Audio Systems Using Kautz Filters
Tuomas Paatero, Matti Karjalainen, *Helsinki University of Technology, Finland*

Automatic Test Image Generation by Genetic Algorithms for Testing Halftoning Methods - Comparing Results Using Wavelet Filtering
Timo Mantere, Jarmo T. Alander, *University of Vaasa, Finland*

Note on Connections between Active Contours and Rayleigh Quotients
Jussi Tohka, *Tampere University of Technology, Finland*

16.30–16.40 Closing of FINSIG'01

18.15–21.15 Evening Reception offered by the City of Espoo at Gumböle Manor
Times are for bus transportation to leave the FINSIG'01 venue and to be back.

FINSIG'01 NON-AUTHOR REGISTRATION FORM

Title (Dr./Ms./Mr.): _____   Initials: _____

Family name:_____

Given name: _____

Name for the name badge: _____

Affiliation: _____

_____

Address: _____

_____

_____

Tel.   _____

Fax: _____

E-mail: _____

Special diet: _____

Registration fee: 250 FIM (42.05 EUROS) (22 % VAT included)

FINSIG'01 registration fee includes: one copy of the CD-ROM Proceedings, 2 coffee breaks, lunch, evening reception (offered by the City of Espoo), and transportation from the FINSIG'01 venue to the evening reception and back.

The registration fee must be paid prior to registration. Payments by bank transfer only.

Pay by bank transfer to:
- Recipient of the payment:
  Helsinki University of Technology, SIG / FINSIG'01
- Sampo Bank Plc, Helsinki, Finland
- Account number: 800017-22347

ALL PAYMENTS MUST CARRY THE FOLLOWING INFORMATION
- **Reference: FINSIG 13250 / 8564**
- **Name of the participant**

Without this information, your registration fee will not be received by the FINSIG'01 organization.

International bank transfers via SWIFT: SWIFT PSPBFIHH to the bank and account above.
The VAT number is FI02459026.

Please, e-mail this form to: tanskane@wooster.hut.fi.

Because the Evening Reception is offered by the City of Espoo, please register at your earliest convenience.

FINSIG'01 organization does not provide accommodations services. Please check FINSIG'01 web site for FINSIG'01 related accommodation.

Please, direct any questions to jarno.tanskanen@hut.fi.